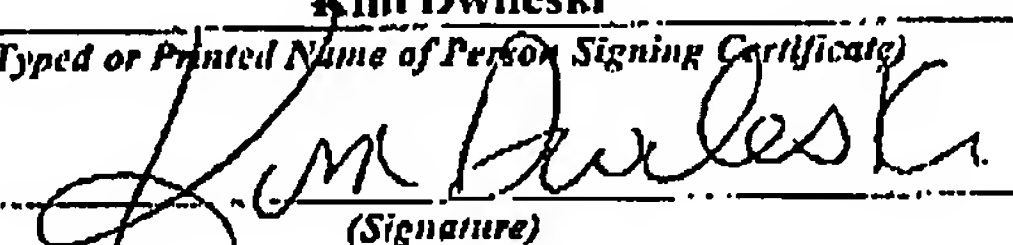


P. 01

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<b>CERTIFICATE OF TRANSMISSION BY FACSIMILE (37 CFR 1.8)</b>			Docket No. <b>BUR920040024US1</b>
Applicant(s): <b>Berstein et al.</b>			
Application No. <b>10/710,453</b>	Filing Date <b>7/13/2004</b>	Examiner <b>Vibol Tan</b>	Group Art Unit <b>2819</b>
Invention: <b>LOW LEAKAGE MONOTONIC CMOS LOGIC</b>			
<p>I hereby certify that this _____ <u><b>Response to Restriction (2 pages)</b></u> _____</p> <p style="text-align: center;"><small>(Identify type of correspondence)</small></p> <p>is being facsimile transmitted to the United States Patent and Trademark Office (Fax. No. <u><b>571-273-8300</b></u> _____)</p> <p>on <u><b>12/01/2005</b></u> _____</p> <p style="text-align: center;"><small>(Date)</small></p> <div style="text-align: right; margin-top: 100px;"> <p><u><b>Kim Dwileski</b></u> _____</p> <p style="text-align: center;"><small>(Typed or Printed Name of Person Signing Certificate)</small></p> <p> _____</p> <p style="text-align: center;"><small>(Signature)</small></p> </div>			
<p><b>Note: Each paper must have its own certificate of mailing.</b></p>			

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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Bernstein et al.

Docket No.: BUR920040024US1

Serial No.: 10/710,453

Art Unit: 2819

Filed: July 13, 2004

Examiner: Vibol Tan

Title: LOW LEAKAGE MONOTONIC CMOS LOGIC

## RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents & Trademarks  
P. O. Box 1450  
Alexandria, VA 22313-1450

In response to the Restriction Requirement mailed on November 22, 2005, Applicant hereby provisionally elects Group I, claims 1-22, drawn to electronic digital logic circuitry, classified in class 326, subclass 81. This election is made with traverse, and Applicants hereby reserve the right to file a divisional application in connection with unelected claims 23-32.

With regard to the Restriction Requirement, Applicants respectfully submit that the subject matter of all claims 1-32 is sufficiently related that a thorough search for the subject matter of any one group of claims would encompass a search for the subject matter of the remaining claims. Thus, Applicants respectfully submit that the search and the examination of the entire application could be made without serious burden. See MPEP § 803, in which it is stated that "if the search and examination of the entire application can be made without serious burden, the Examiner must examine it on the merits, even though it includes claims to independent or distinct inventions" (emphasis added). Applicants respectfully submit that this policy should apply in the present application in order to avoid unnecessary delay and expense to Applicants and duplicative examination by the Patent Office.

S/N 10/710,453

1

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The Director is hereby authorized to charge and/or credit Deposit Account 09-0456.

Should the Examiner require or request anything further from Applicant's prior to examination, the Examiner is requested to contact Applicants' undersigned representative at the Agent Direct Dial telephone number below. Otherwise, Applicants request early and favorable examination on the merits

Respectfully submitted,  
FOR: Bernstein et al.

Dated: 12/01/2005

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